

Advance data

Insulated Gate Bi-Polar Transistor

Type T1200TD25A

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{CES}	Collector – emitter voltage	2500	V
$V_{DC\ link}$	Permanent DC voltage for 100 FIT failure rate.	1250	V
V_{GES}	Peak gate – emitter voltage	± 20	V

	RATINGS	MAXIMUM LIMITS	UNITS
I_C	DC collector current, IGBT	1200	A
I_{CRM}	Repetitive peak collector current, $t_p=1ms$, IGBT	2400	A
$I_{F(DC)}$	Continuous DC forward current, Diode	1200	A
I_{FRM}	Repetitive peak forward current, $t_p=1ms$, Diode	2400	A
I_{FSM}	Peak non-repetitive surge $t_p=10ms$, $V_{RM}=60\%V_{RRM}$, Diode (Note 4)	7240	A
I_{FSM2}	Peak non-repetitive surge $t_p=10ms$, $V_{RM}\leq 10V$, Diode (Note 4)	7960	A
P_{MAX}	Maximum power dissipation, IGBT (Note 2)	5.9	kW
$(di/dt)_{cr}$	Critical diode di/dt (note 3)	2500	A/ μs
T_j	Operating temperature range.	-40 to +125	$^{\circ}C$
T_{stg}	Storage temperature range.	-40 to +125	$^{\circ}C$

Notes: -

- 1) Unless otherwise indicated $T_j = 125^{\circ}C$.
- 2) $T_{sink} = 25^{\circ}C$, double side cooled.
- 3) Maximum commutation loop inductance 375nH.
- 4) Half-sinewave, $125^{\circ}C$ T_j initial.

Characteristics

IGBT Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
V _{CE(sat)}	Collector – emitter saturation voltage	-	2.45	2.75	I _C = 1200A, V _{GE} = 15V, T _j = 25°C	V
		-	3.15	3.45	I _C = 1200A, V _{GE} = 15V	V
V _{T0}	Threshold voltage	-	-	1.42	Current range: 400A – 1200A	V
r _T	Slope resistance	-	-	1.69		mΩ
V _{GE(TH)}	Gate threshold voltage	-	5.8	-	V _{CE} = V _{GE} , I _C = 100mA	V
I _{CES}	Collector – emitter cut-off current	-	10	30	V _{CE} = V _{CES} , V _{GE} = 0V	mA
I _{GES}	Gate leakage current	-	-	±20	V _{GE} = ±20V	µA
C _{ies}	Input capacitance	-	150	-	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz	nF
t _{d(on)}	Turn-on delay time	-	0.75	-	I _C = 1200A, V _{CE} = 1250V, di/dt = 2000A/µs	µs
t _{r(V)}	Rise time	-	2.1	-		µs
Q _{g(on)}	Turn-on gate charge	-	8	-	V _{GE} = ±15V, L _s = 330nH	µC
E _{on}	Turn-on energy	-	1.5	-	R _{g(ON)} = 3Ω, R _{g(OFF)} = 6.2Ω, C _{GE} = 47nF	J
t _{d(off)}	Turn-off delay time	-	1.3	-	Integral diode used as freewheel diode (Note 3 & 4)	µs
t _{f(l)}	Fall time	-	2.5	-		µs
Q _{g(off)}	Turn-off gate charge	-	6.5	-		µC
E _{off}	Turn-off energy	-	2.1	-		J
I _{SC}	Short circuit current	-	5000	-	V _{GE} = +15V, V _{CC} = 1250V, V _{CEmax} ≤ V _{CES} , t _p ≤ 10µs	A

Diode Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
V _F	Forward voltage	-	2.45	2.75	I _F = 1200A, T _j = 25°C	V
		-	2.5	2.8	I _F = 1200A	V
V _{To}	Threshold voltage	-	-	1.59	Current range 400A - 1200A	V
r _T	Slope resistance	-	-	1.01		mΩ
I _{rm}	Peak reverse recovery current	-	800	-	I _F = 1200A, V _r = 1250V, V _{GE} = -15V, di/dt = 2000A/µs	A
Q _{rr}	Recovered charge	-	840	-		µC
t _{rr}	Reverse recovery time, 50% chord	-	0.97	-		µs
E _r	Reverse recovery energy	-	0.76	-		J

Thermal Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
R _{thJK}	Thermal resistance junction to sink, IGBT	-	-	16.9	Double side cooled	K/kW
		-	-	26.5	Collector side cooled	K/kW
		-	-	49	Emitter side cooled	K/kW
R _{thJK}	Thermal resistance junction to sink, Diode	-	-	29.2	Double side cooled	K/kW
		-	-	45.2	Cathode side cooled	K/kW
		-	-	84	Anode side cooled	K/kW
F	Mounting force	15	-	25	Note 2	kN
W _t	Weight	-	1.2	-		kg

Notes:-

- 1) Unless otherwise indicated T_j = 125°C.
- 2) Consult application note 2008AN01 for detailed mounting requirements
- 3) C_{GE} is additional gate – emitter capacitance added to output of gate drive
- 4) Figures 6 to 9 are obtained using integral diode as freewheeling diode

Curves

Figure 1 – Typical collector-emitter saturation voltage characteristics

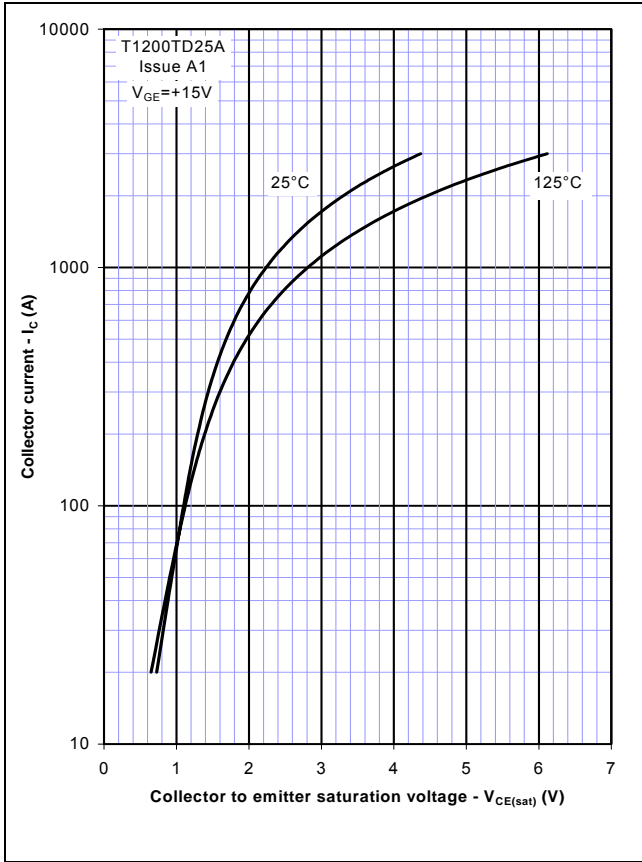


Figure 2 – Typical output characteristic

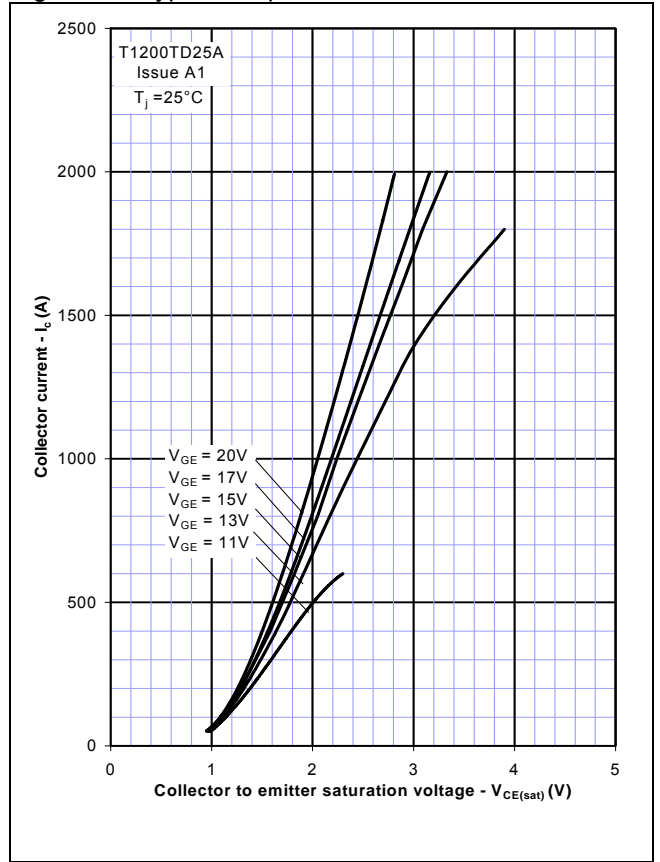


Figure 3 – Typical output characteristic

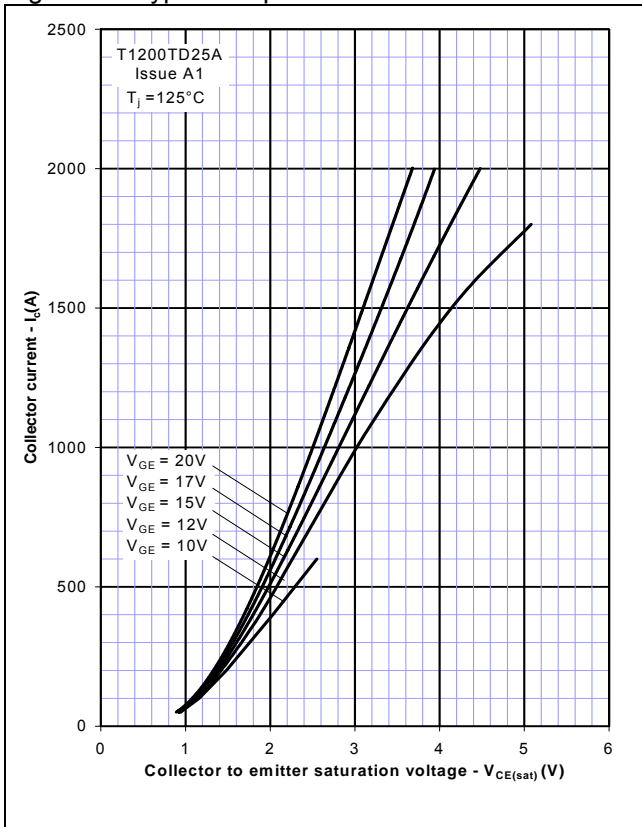


Figure 4 – Typical turn-on delay time vs gate resistance

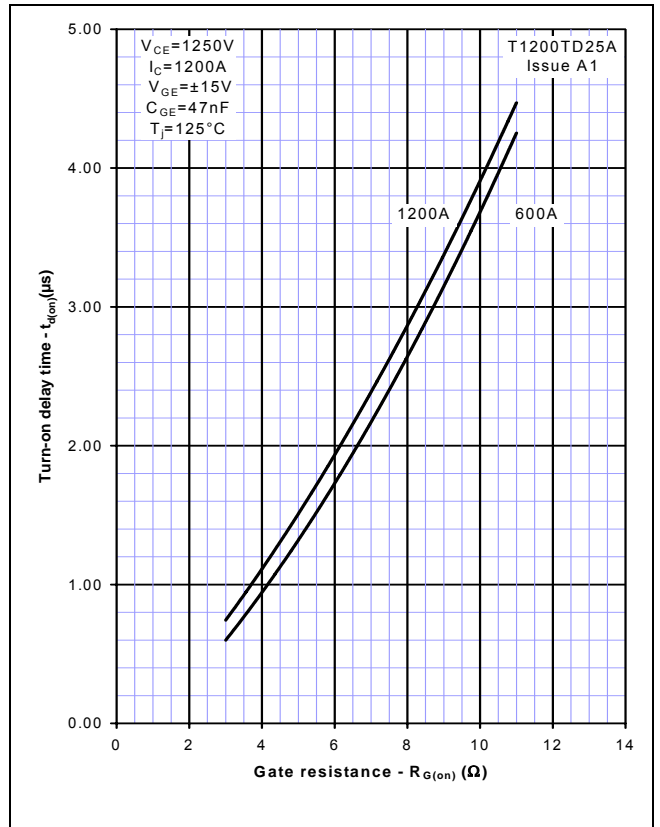


Figure 5 – Typical turn-off delay time vs. gate resistance

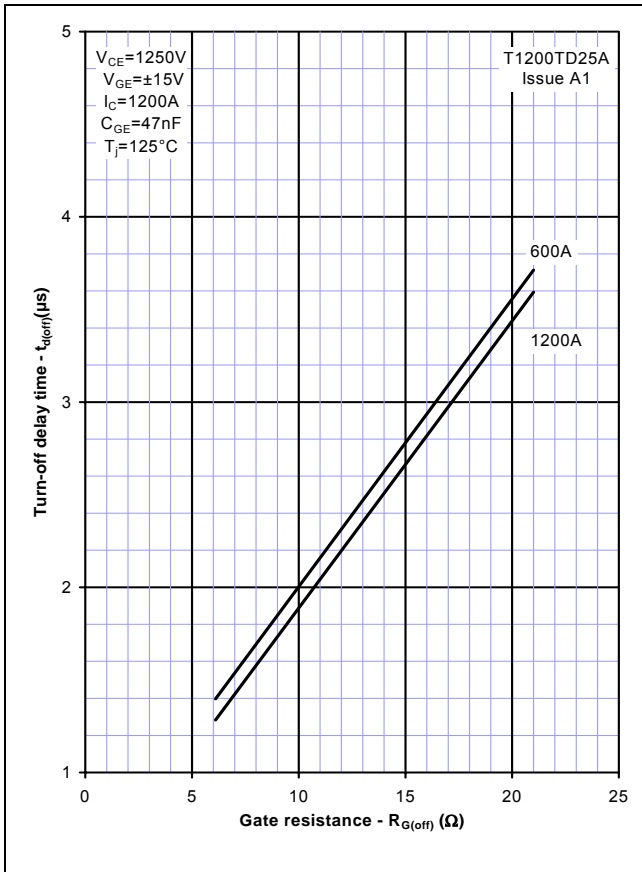


Figure 6 – Typical turn-on energy vs. collector current

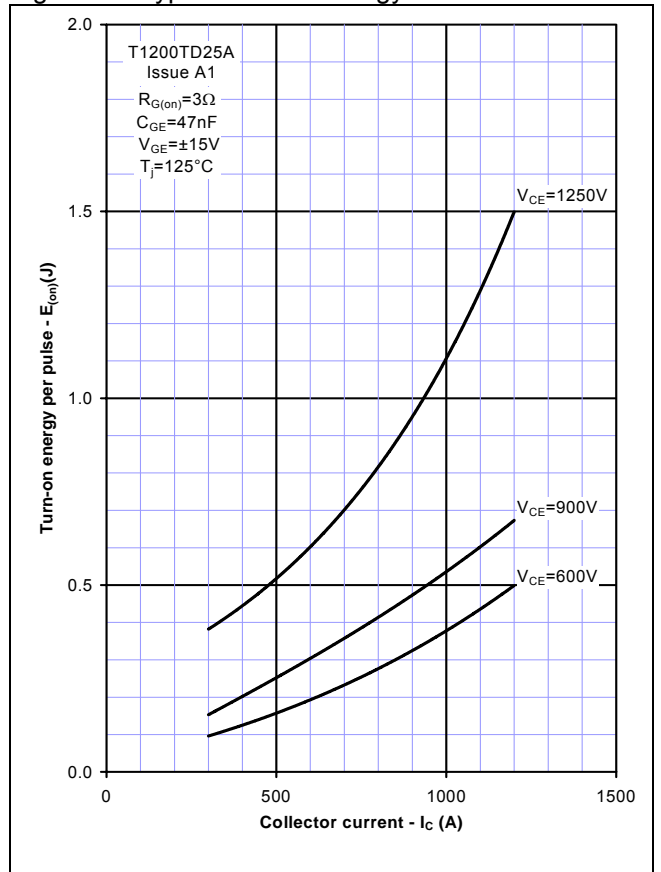


Figure 7 – Typical turn-on energy vs. di/dt

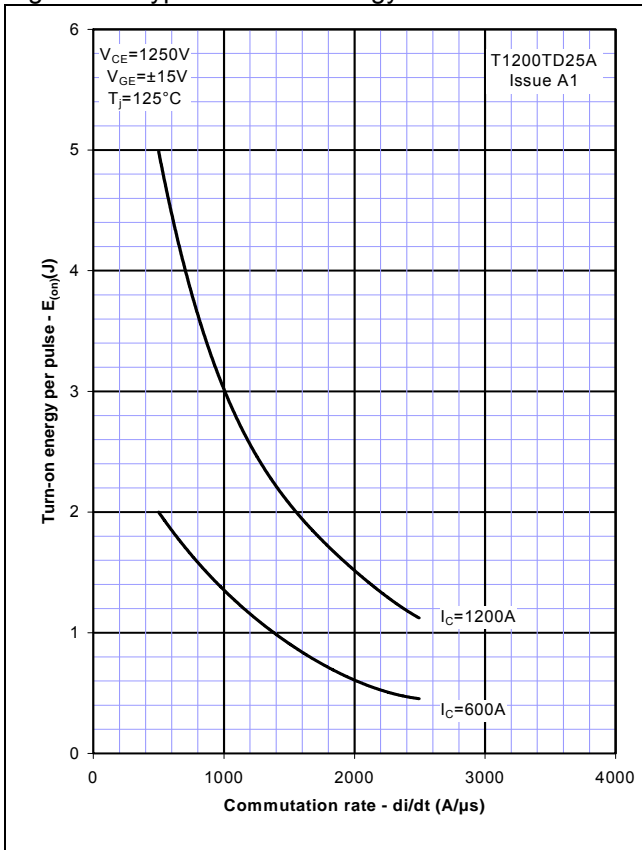


Figure 8 – Typical turn-off energy vs. collector current

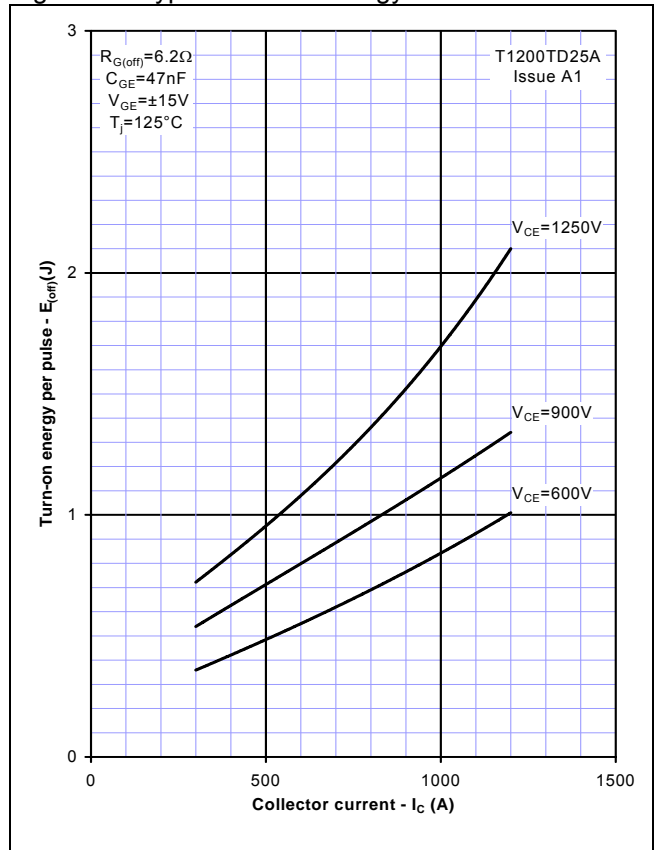


Figure 9 – Turn-off energy vs voltage

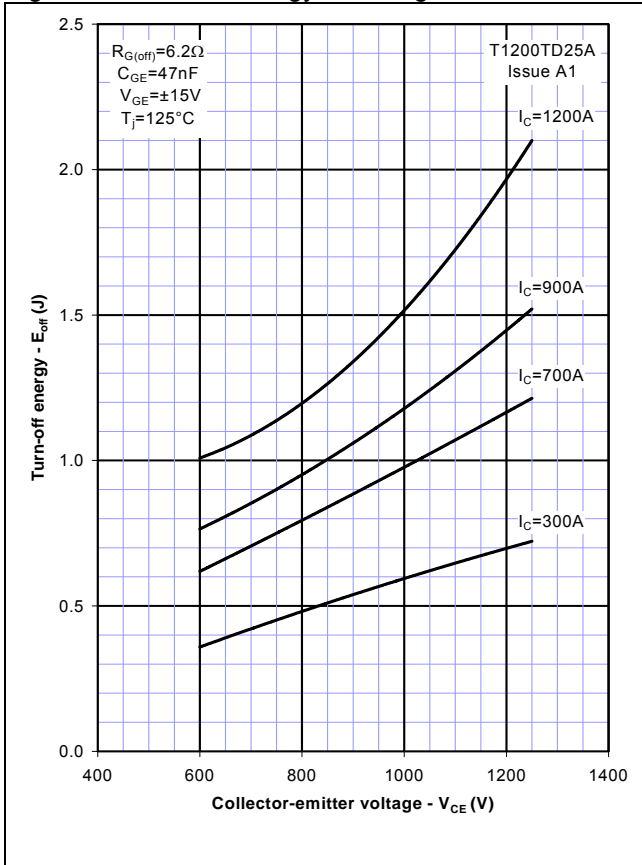


Figure 10 – Safe operating area (IGBT)

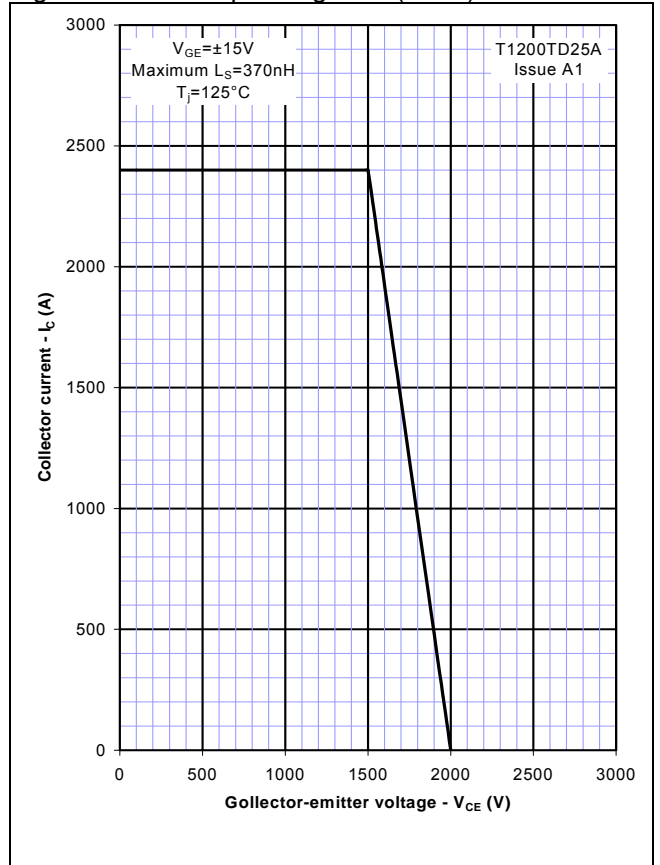


Figure 11 – Typical diode forward characteristics

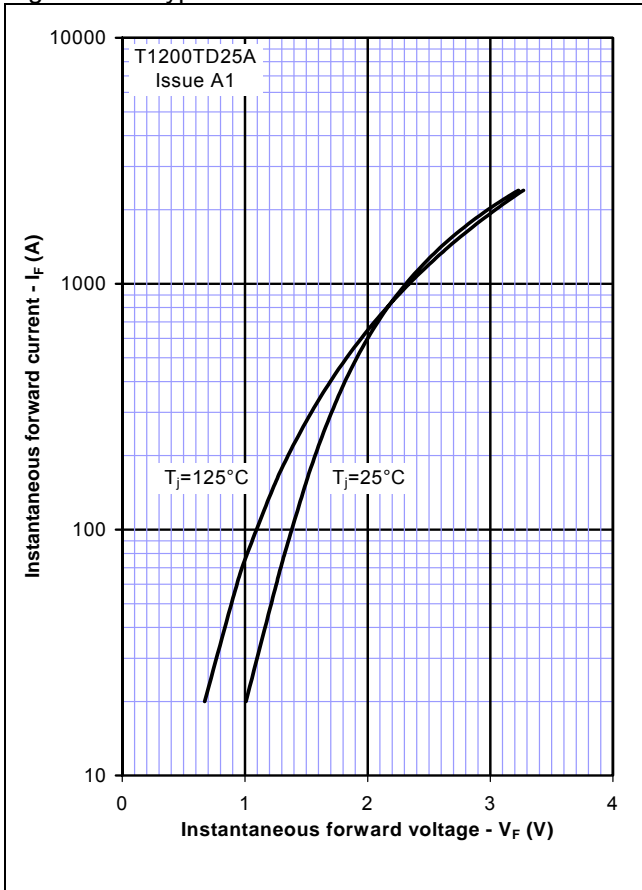


Figure 12 – Typical recovered charge

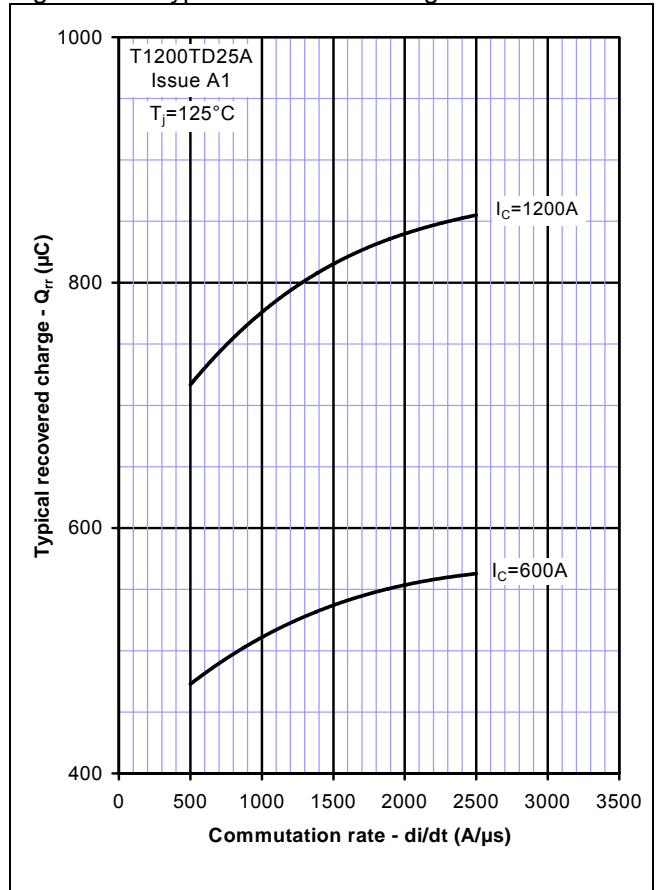


Figure 13 – Typical reverse recovery current

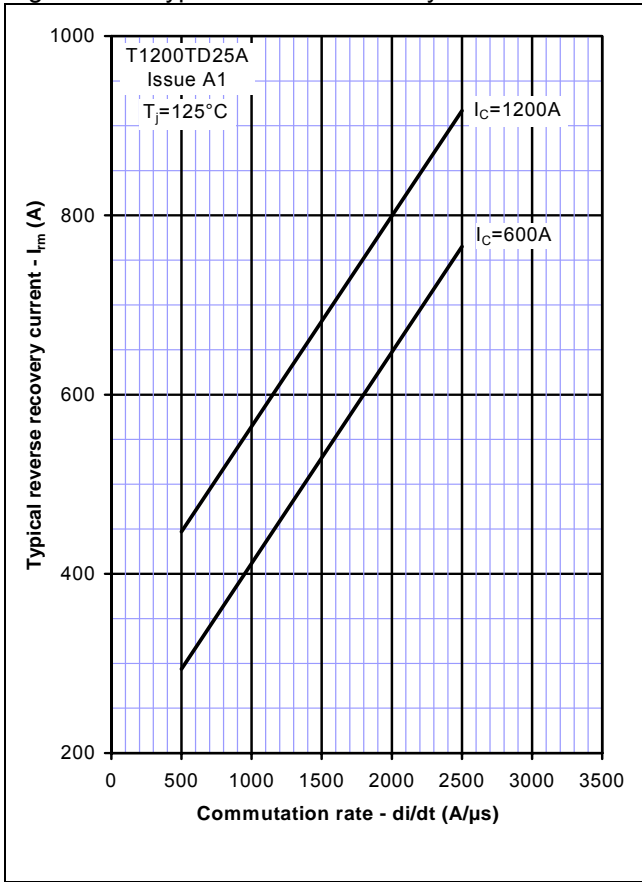


Figure 14 – Typical reverse recovery time

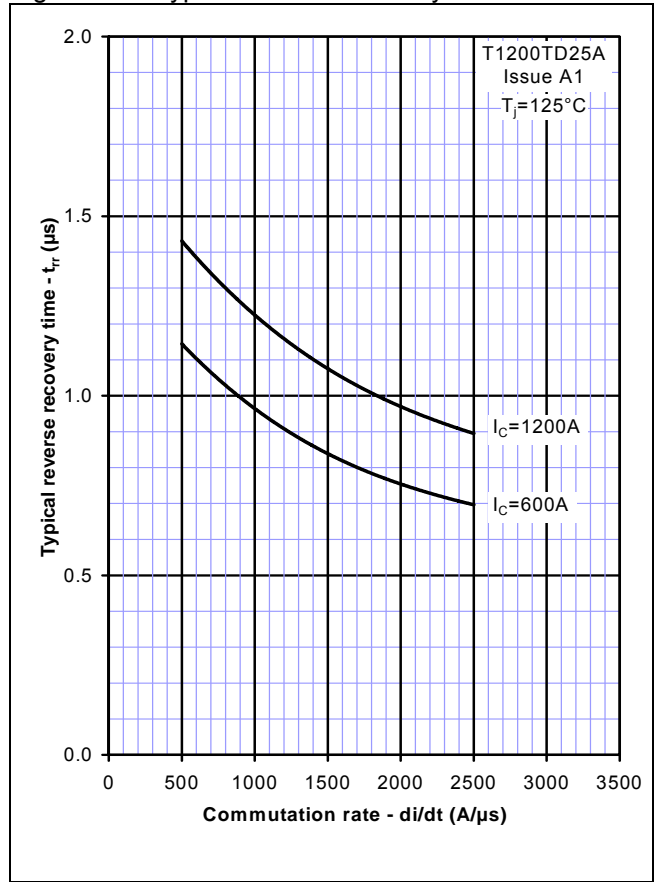


Figure 15 – Typical reverse recovery energy

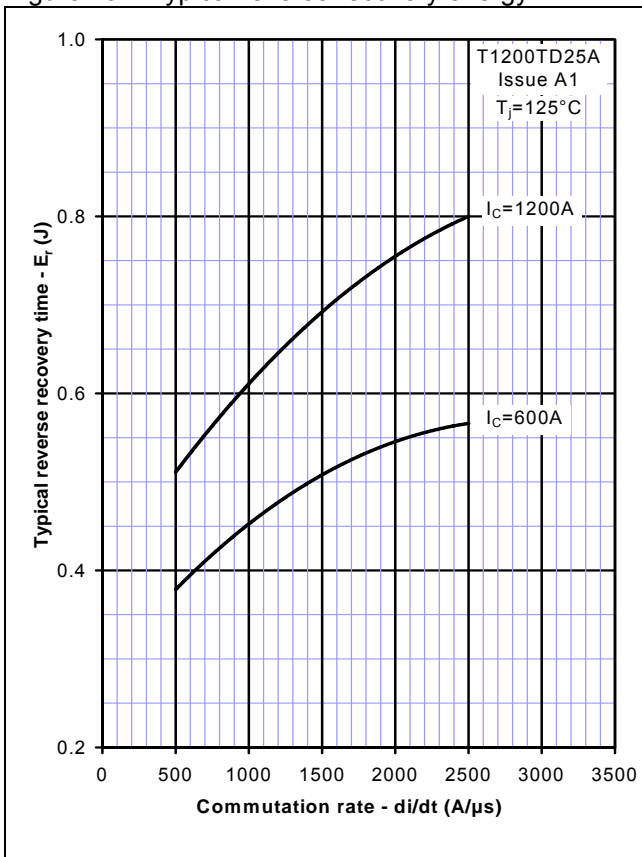


Figure 16 – Safe operating area (Diode)

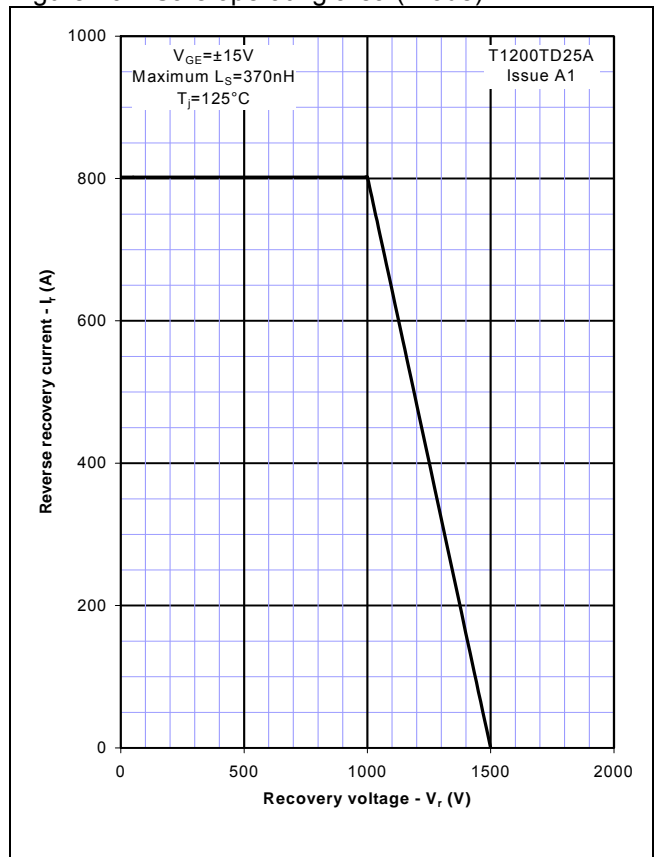


Figure 17 – Transient thermal impedance (IGBT)

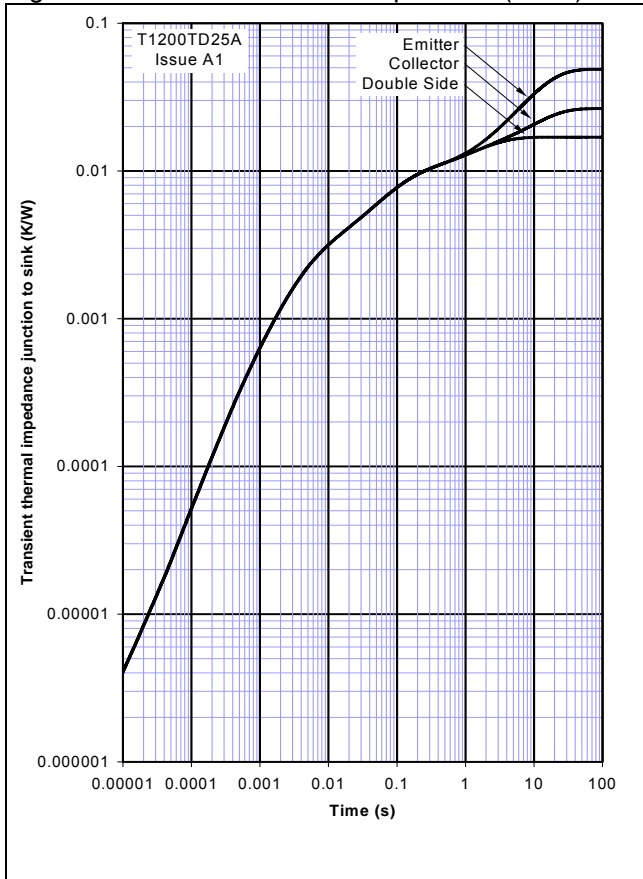
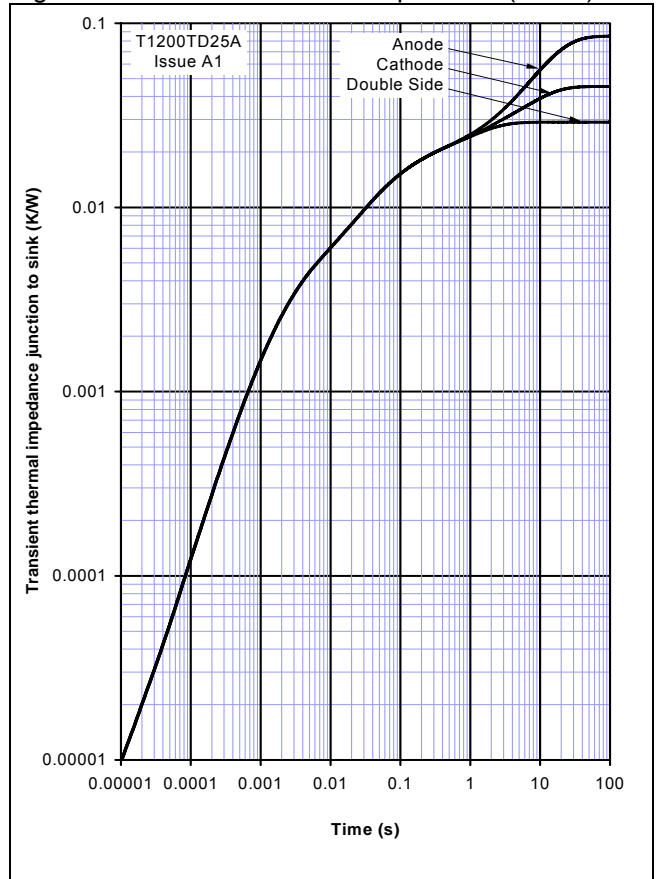
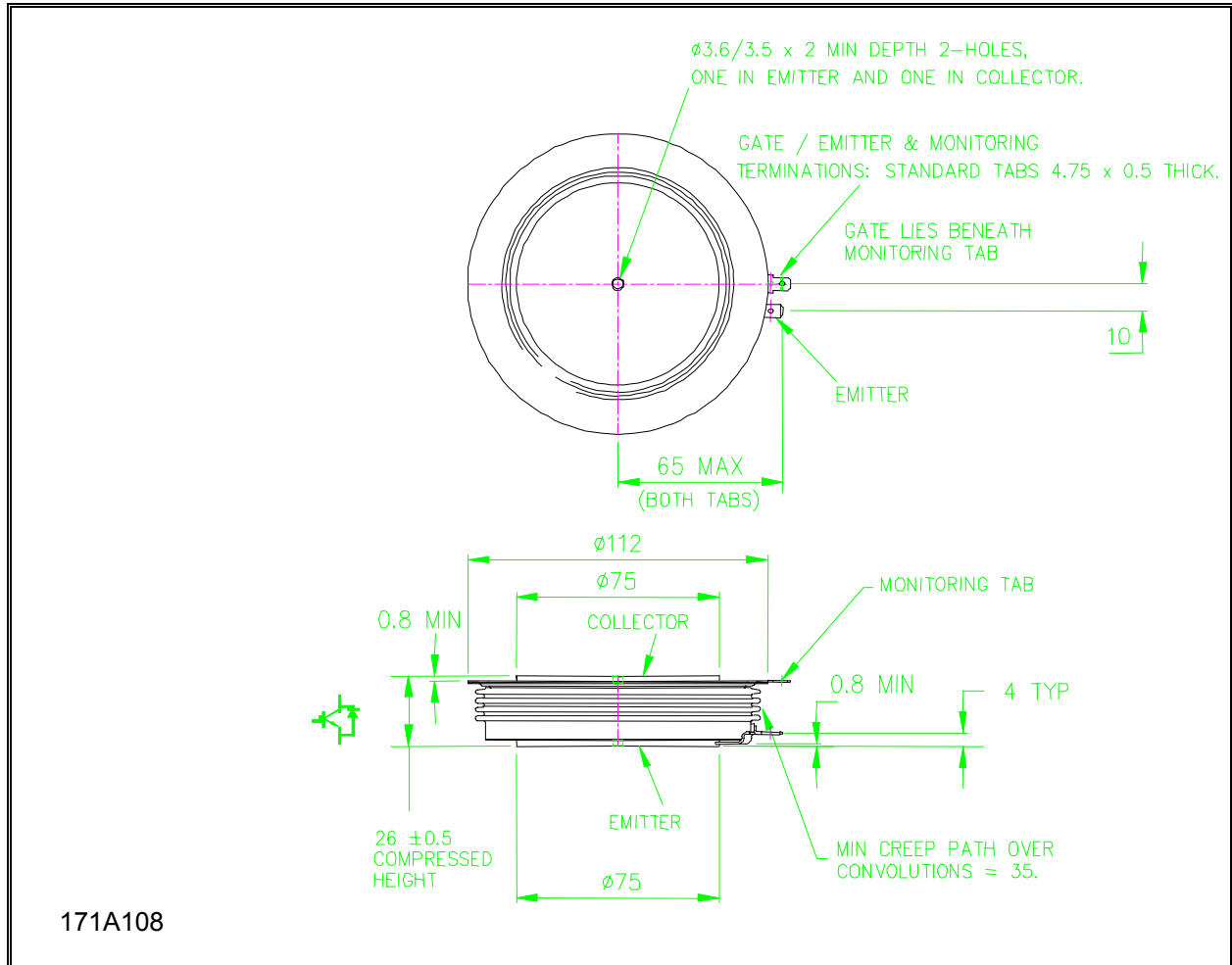


Figure 18 – Transient thermal impedance (Diode)



Outline Drawing & Ordering Information



ORDERING INFORMATION

(Please quote 10 digit code as below)

T1200	TD	25	A
Fixed type Code	Fixed Outline Code	Voltage Grade $V_{CES}/100$ 25	Fixed format code

Typical order code: T1200TD25A ($V_{CES} = 2500V$)

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